

Fig.1

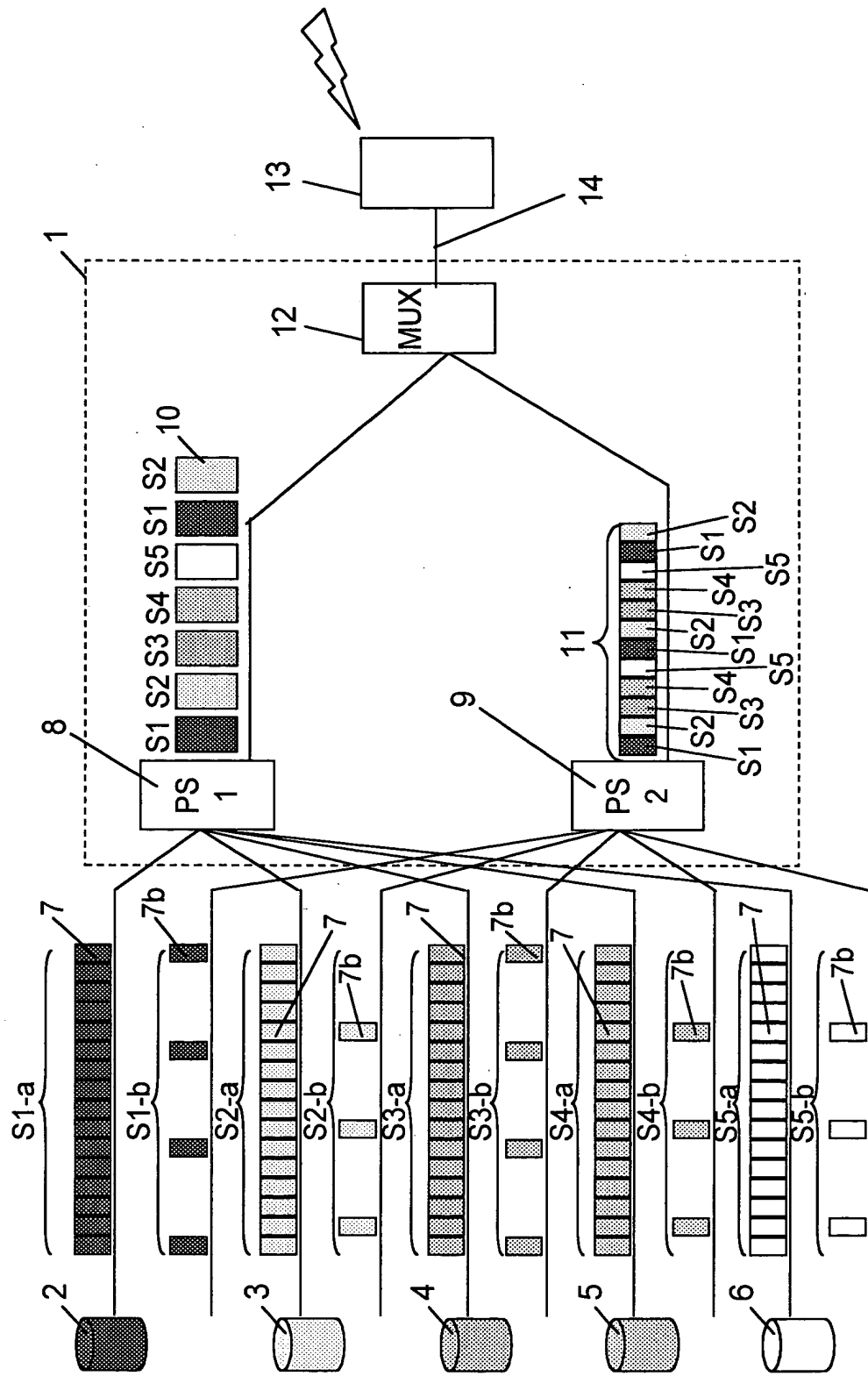


Fig.2

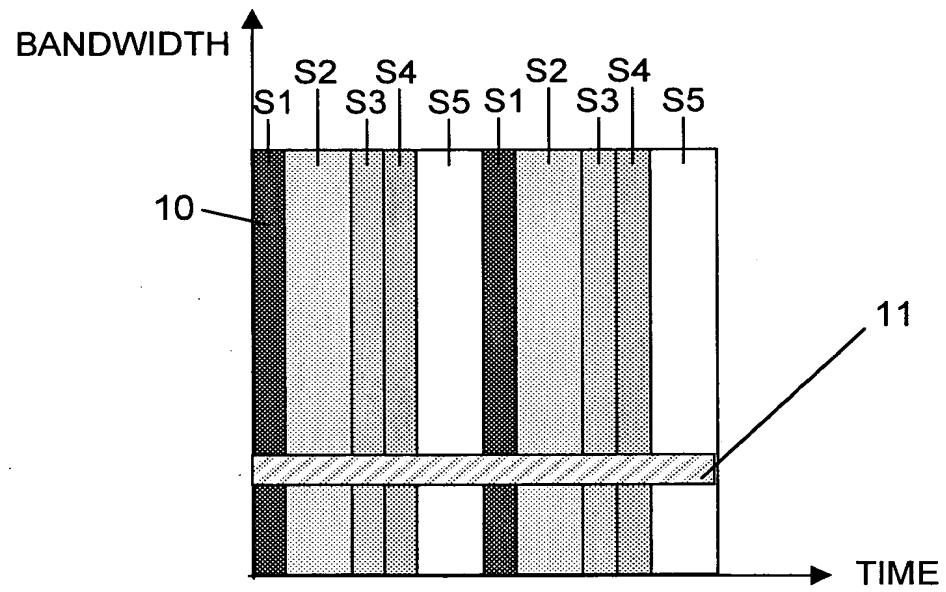


Fig.3

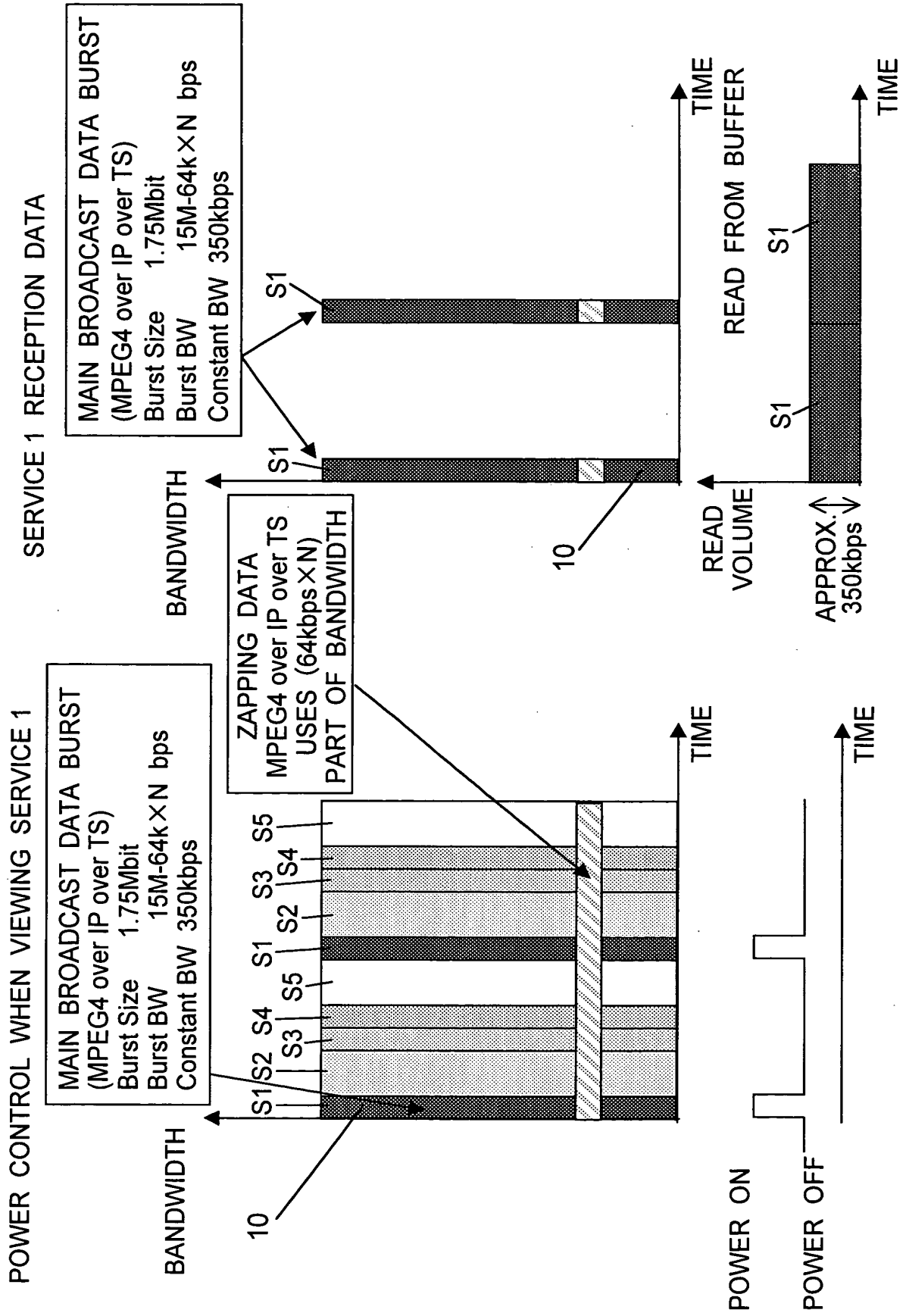


Fig. 4

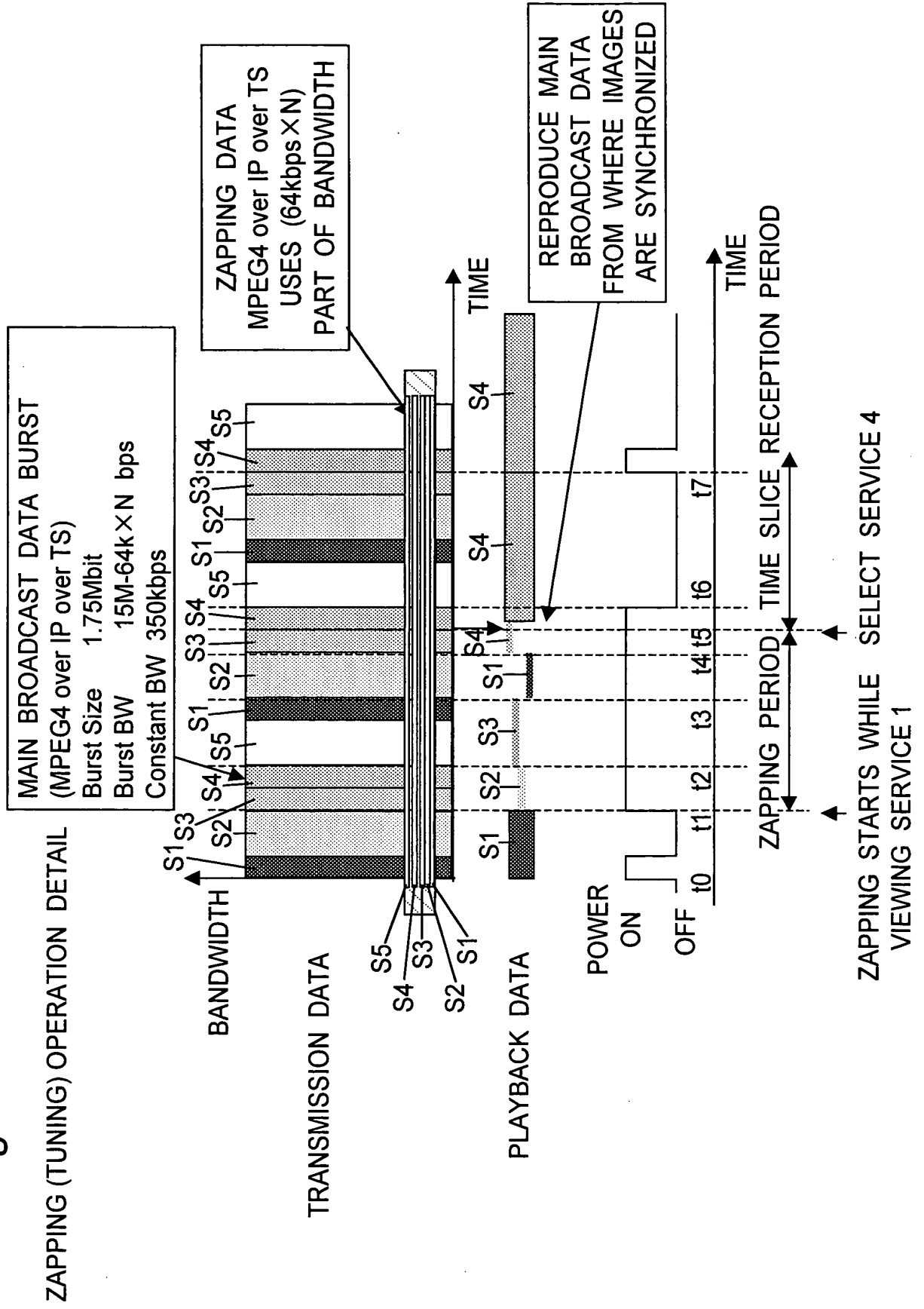


Fig.5

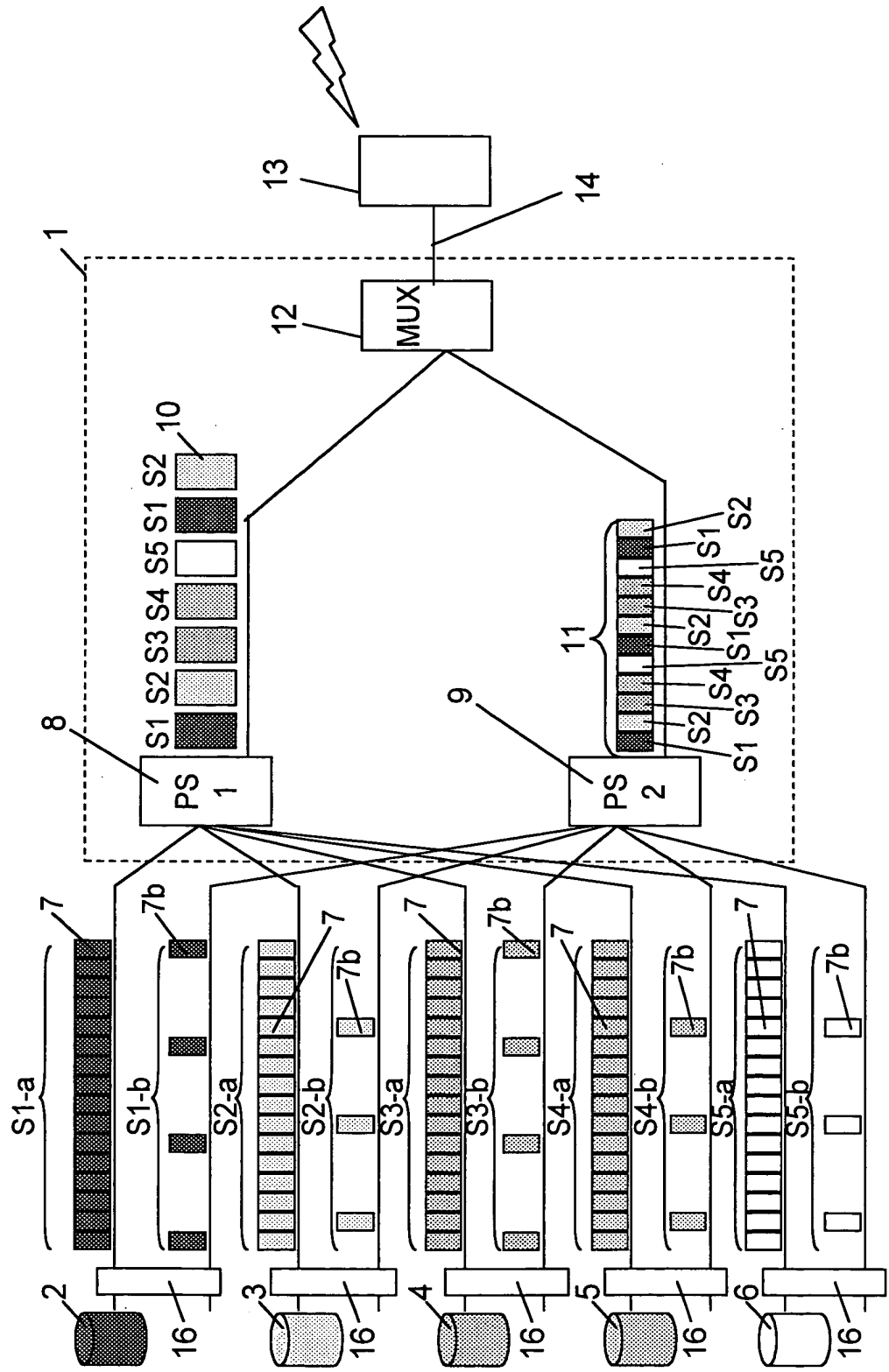


Fig.6

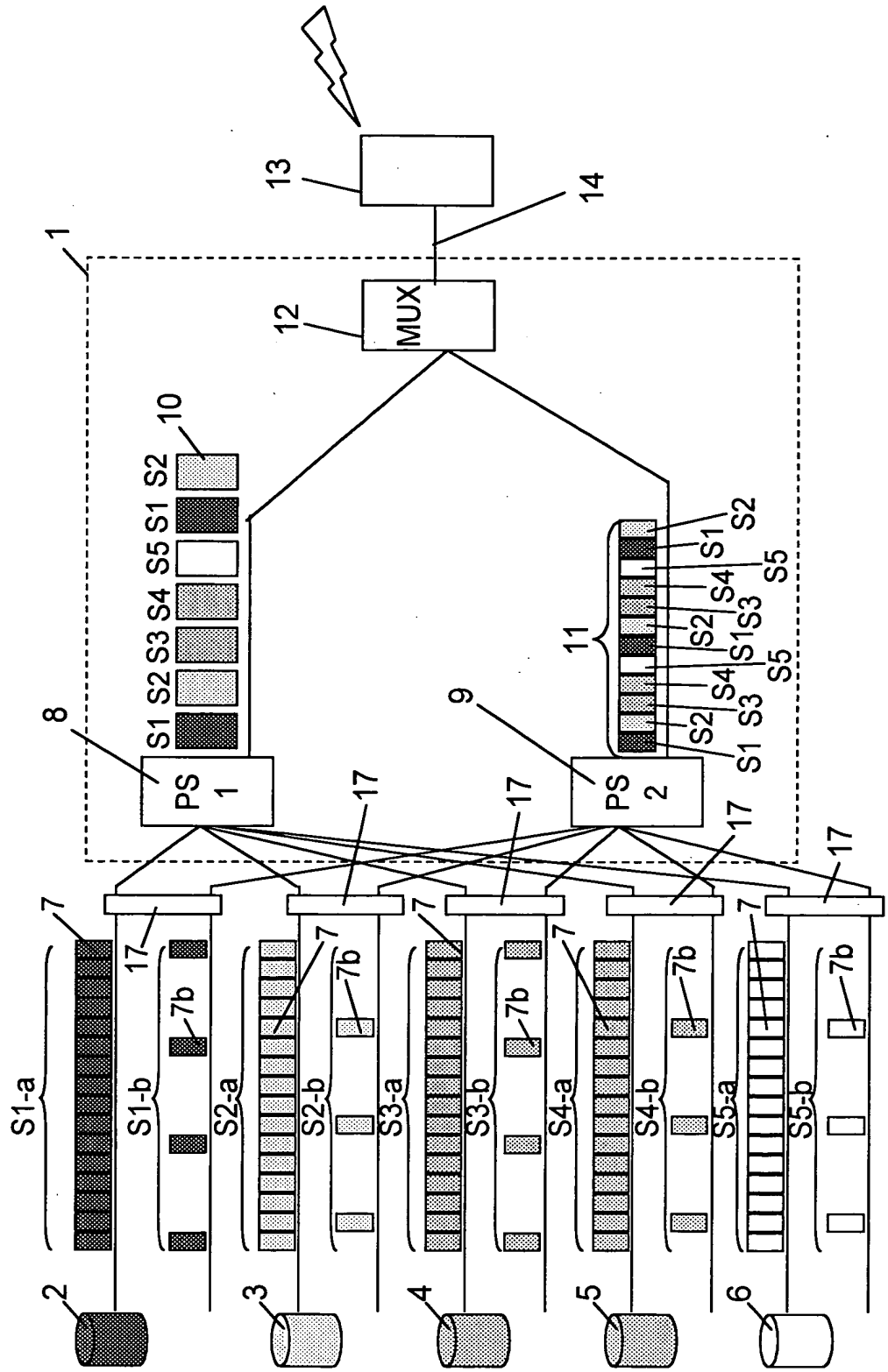


Fig. 7

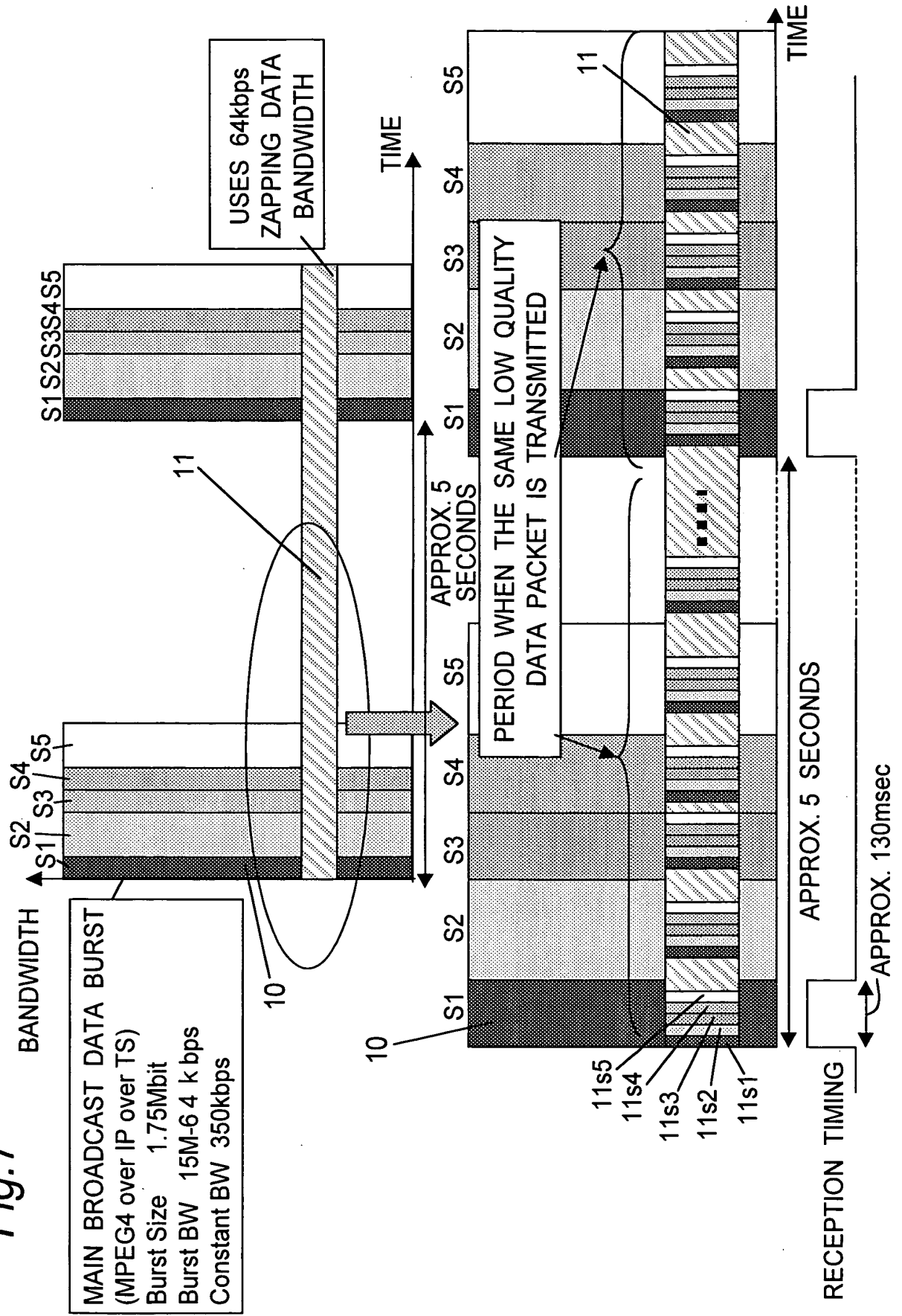


Fig.8

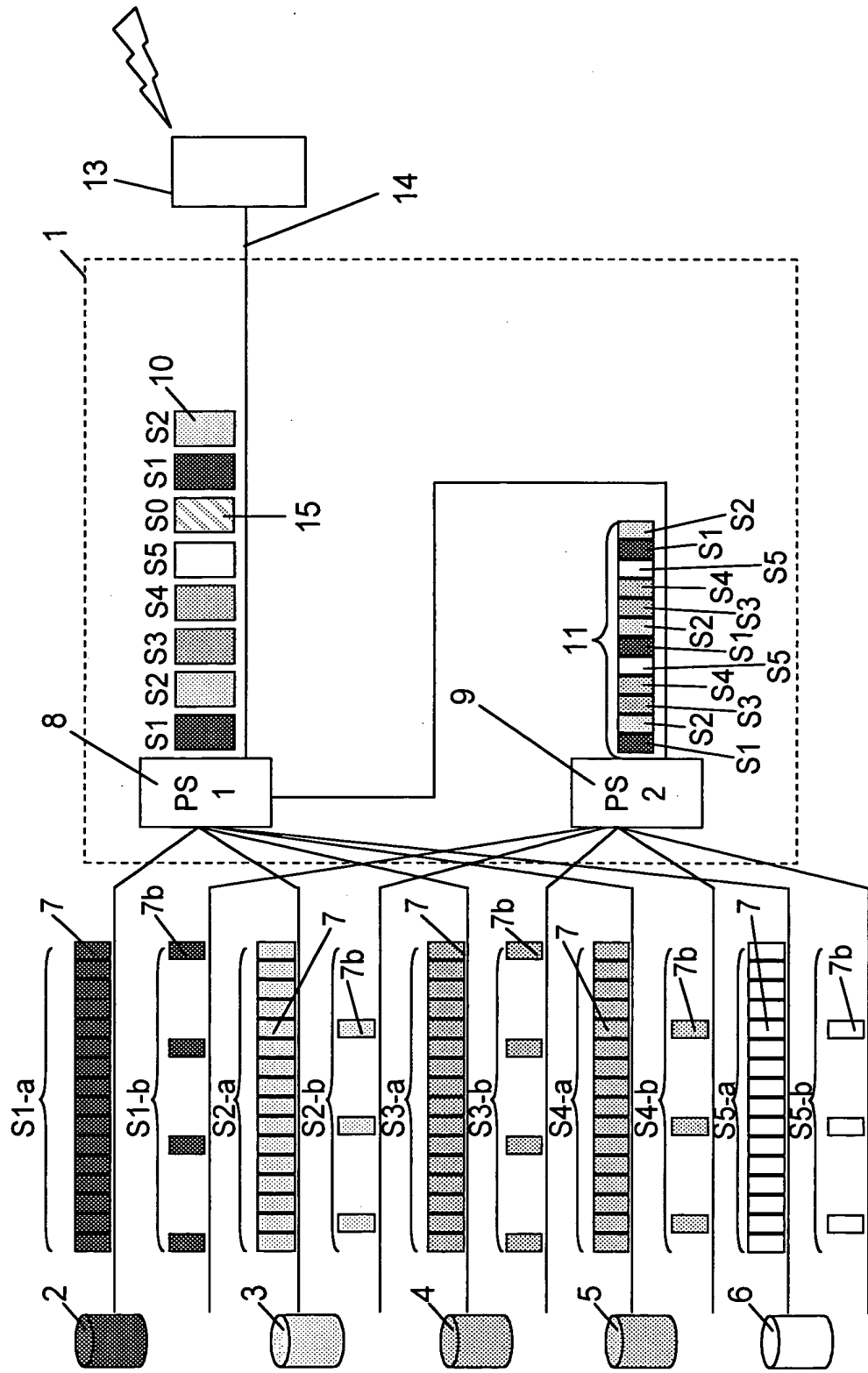


Fig.9

TRANSMISSION IMAGE

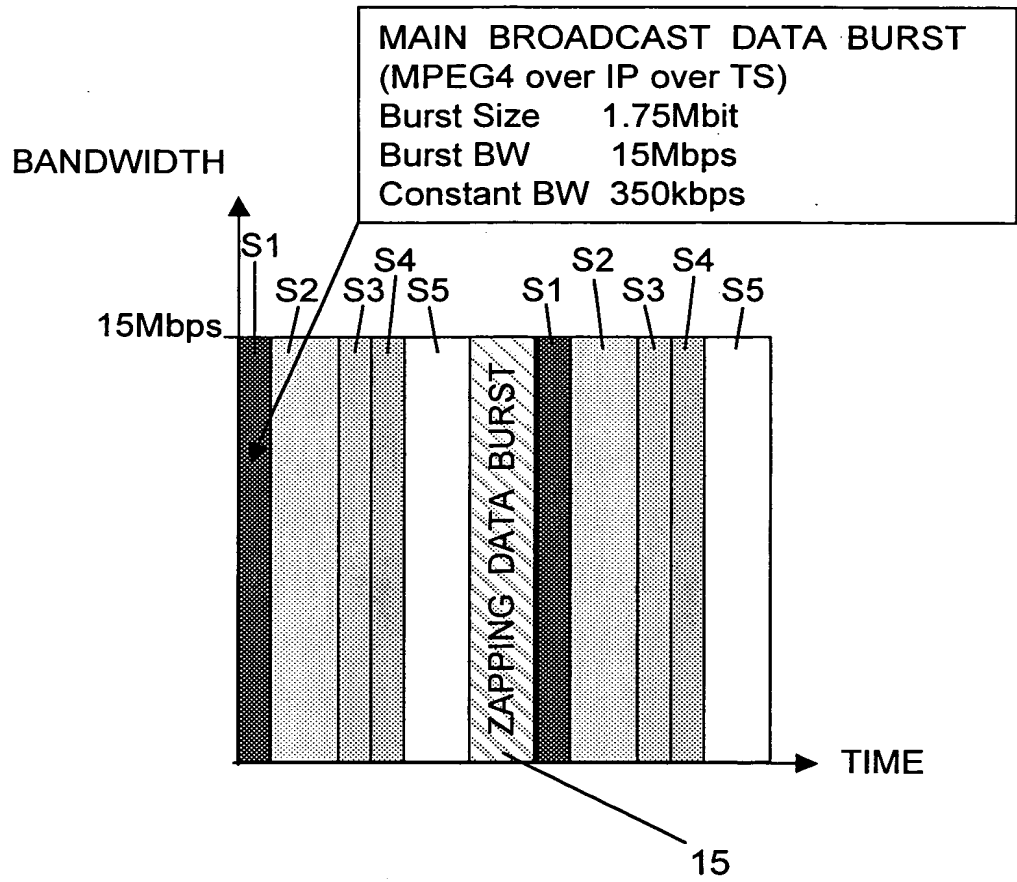


Fig.10

DETAILED OPERATION
WHEN RECEIVING
A ZAPPING
DATA BURST

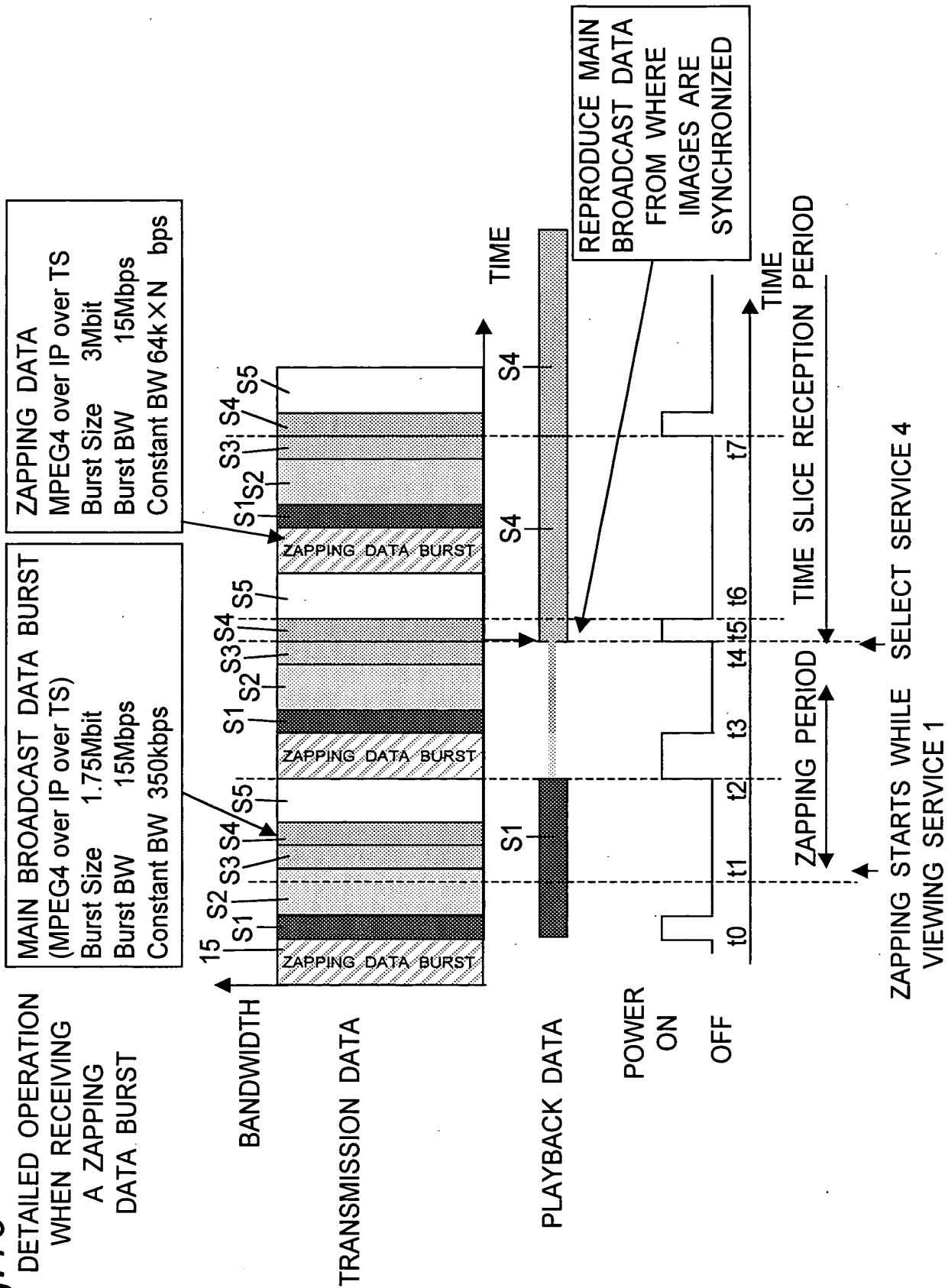


Fig.11

POWER CONTROL DURING TIME SLICE TRANSMISSION

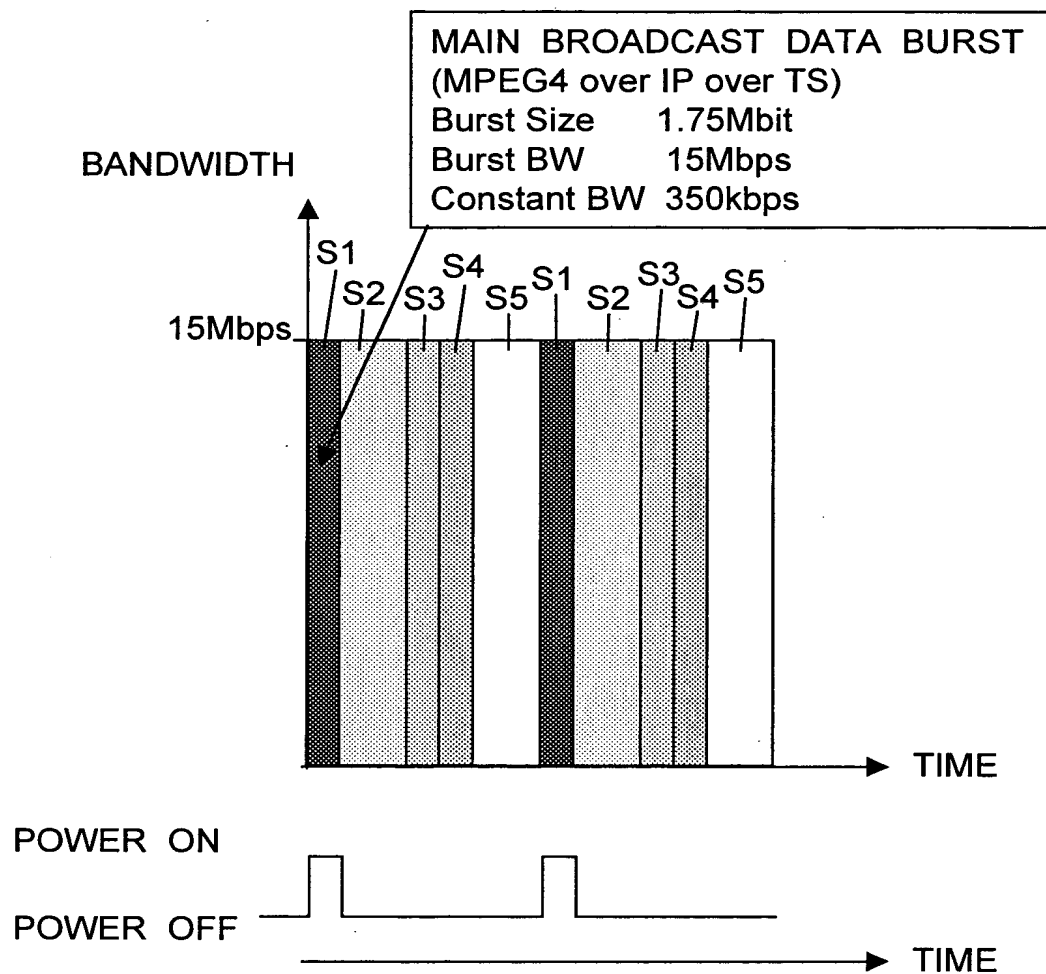


Fig.12

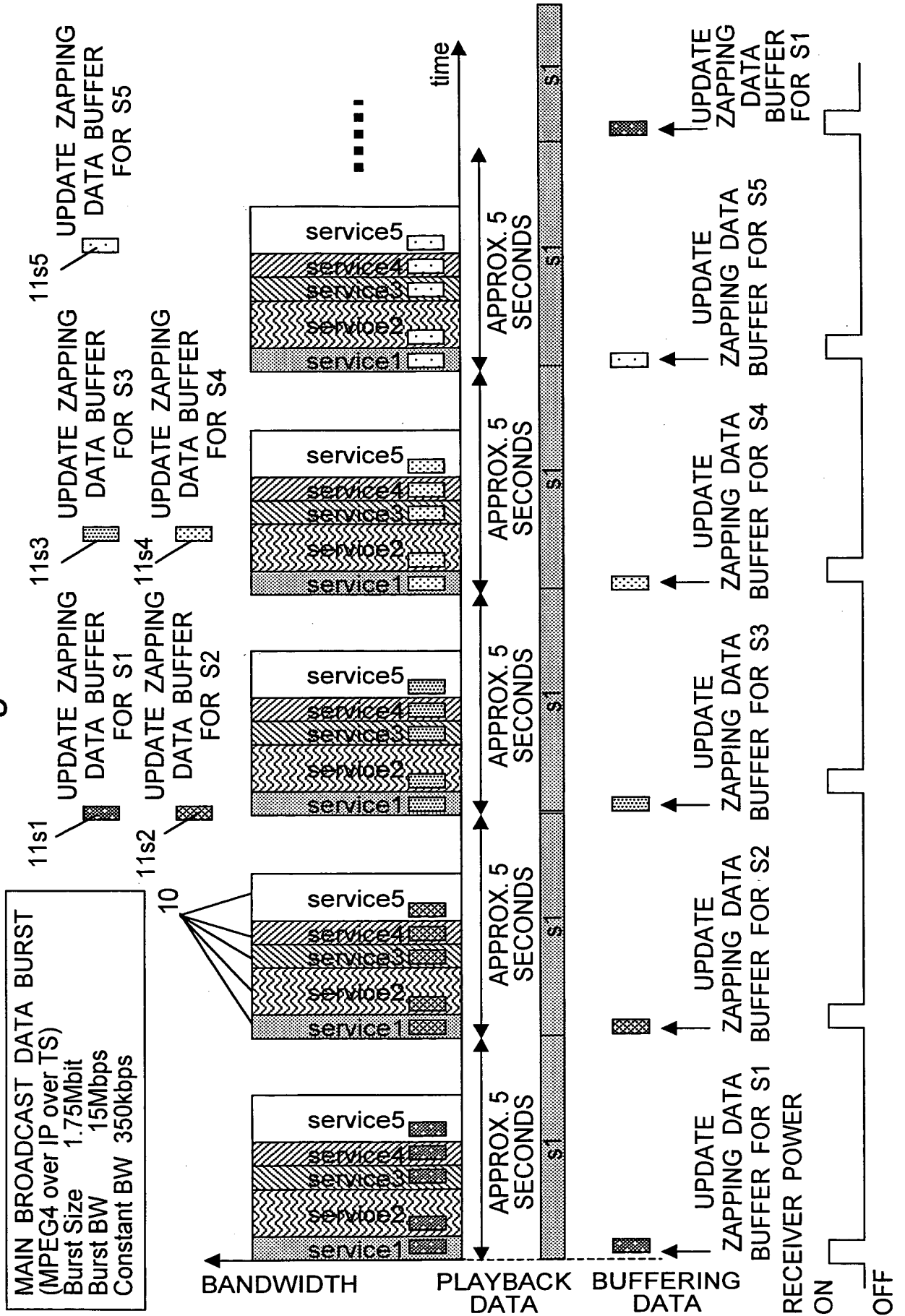


Fig. 13

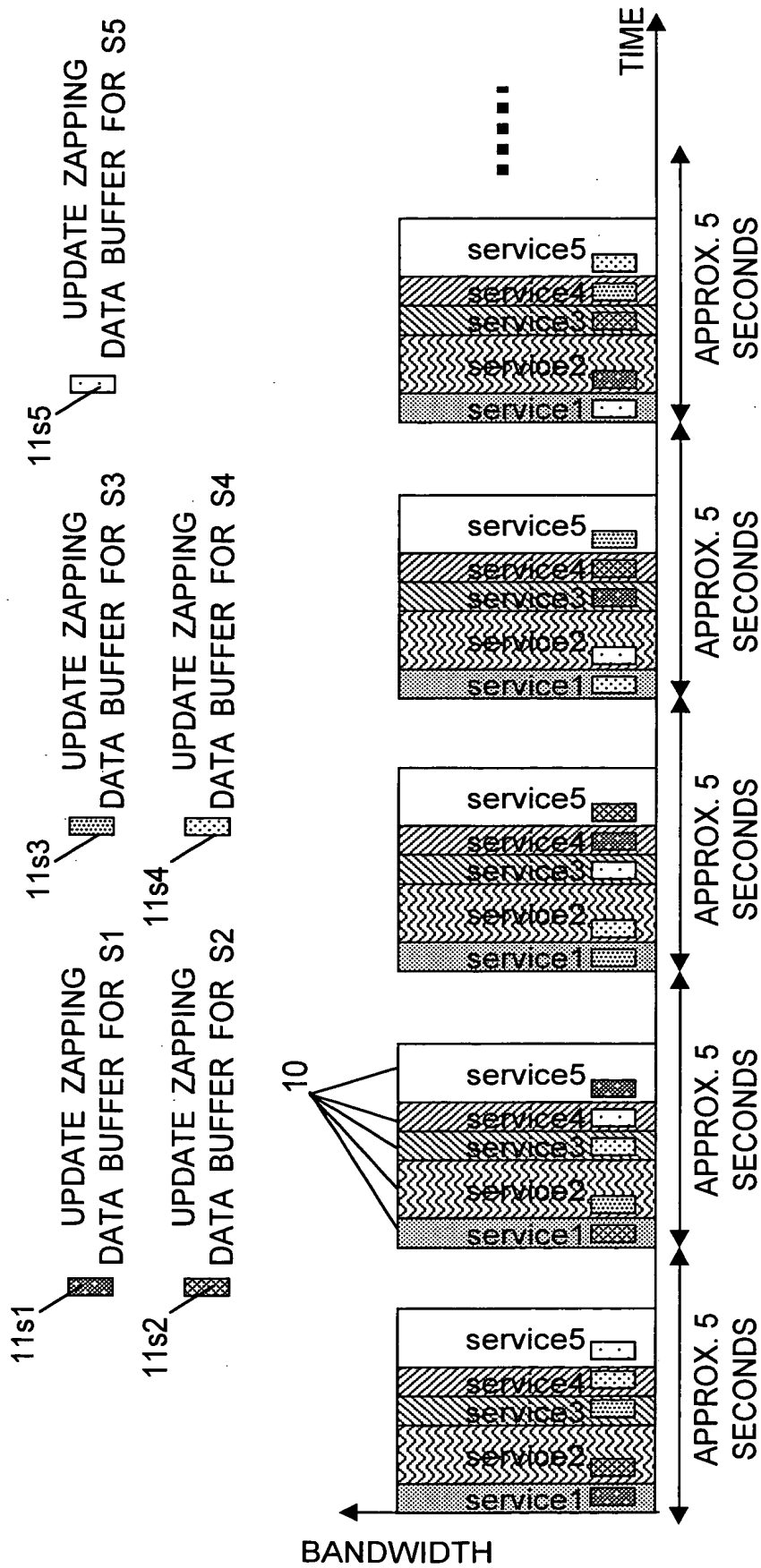


Fig. 14

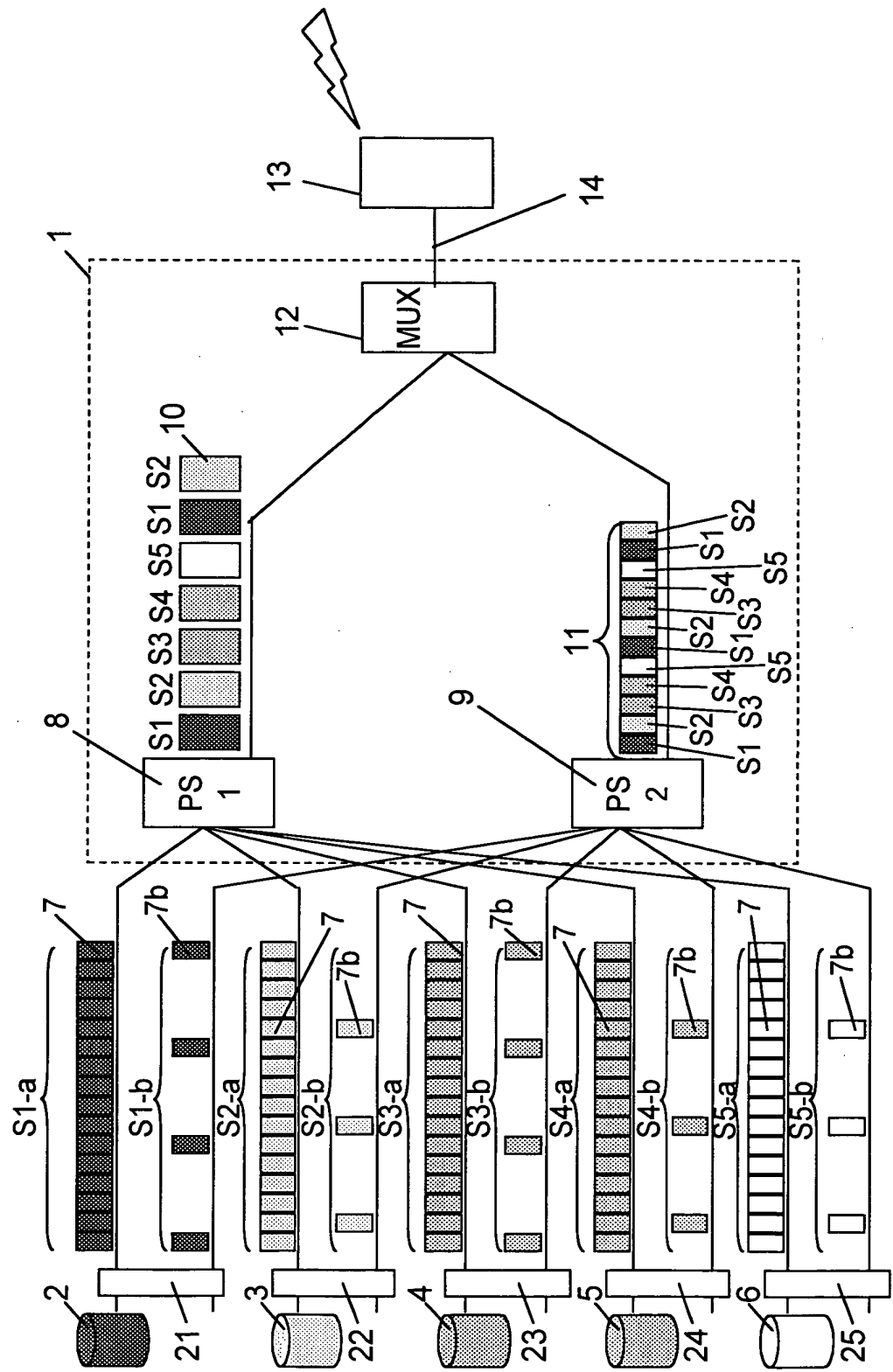


Fig. 15

